## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	1	(cache adj miss) near8 (mirror\$3 adj cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/03/01 10:25
L12	1	(cache adj miss) near8 (redundant adj cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/03/01 10:15
L13	636	(cache adj miss) near8 ("I2" or "level 2" cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/03/01 10:16
L14	560	(cache adj miss) near8 (("I2" or "level 2") cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/03/01 10:16
L15	1	(cache adj miss) near8 ("I2" or "level 2" cache) and 711/162.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/03/01 10:19
L16	138	(cache adj miss) with ((access or read\$3) near4 cache) near (controller or host or adaptor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/03/01 10:20
L18	85	(cache adj miss) with ((access or read\$3) near4 cache) near (controller or host or adaptor) and 711/???.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/03/01 10:21
L21	42	(cache near2 miss) with ((access\$3 or read\$3) near6 (second near3 (controller or adaptor or processor or host)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/03/01 10:27